

JC6/572- 10/06/92 16/06/92 00/29/02

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10064921	FILING DATE 08/29/2002	CLASS 326	SUBCLASS 95	GAU 2819	EXAMINER A. Tran
**APPLICANTS: Bernstein Kerry; Emma Philip; Fifield John; Kartschoke Paul; Rohrer Norman; Sandon Peter;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO BUR920010204	
TITLE : Apparatus for reducing soft errors in dynamic circuits					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims		Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.
			Application Examiner		
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